Data Sheet

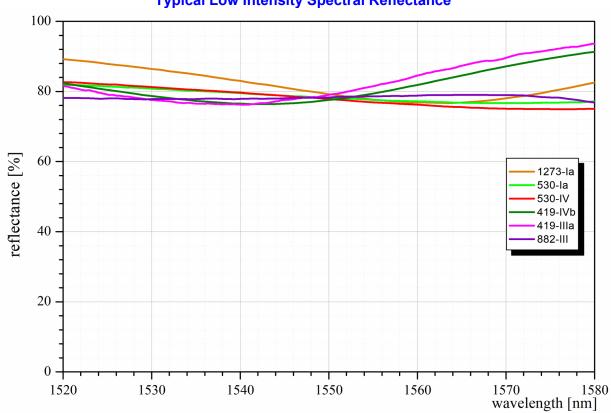


SAMTM Data Sheet SAM-1550-20-3ps-15-x, λ = 1550 nm

	Minimum	Typical Value	Maximum
Operational wavelength λ	-	1550 nm	-
High reflection band	1520 nm	-	1580 nm
Absorbance A	16 %	20 %	24 %
Modulation depth ΔR	7%	10 %	-
Non-saturable loss A _{ns}	-	10 %	13%
Saturation fluence Φ_{sat}	10 μJ/cm²	15 μJ/cm²	25 µJ/cm²
Relaxation time constant τ	1.5 ps	3 ps	5 ps
Damage threshold Φ		800 μJ/cm²	
Absorber Peak Temperature			150°C ¹
Chip thickness	425 µm	450 µm	475 μm
Protection	SAM is protected with a dielectric front layer		

This temperature is not to be exceeded in any circumstances.





Data Sheet



Mounting Options			
SAM-1550-20-3ps-15-x	Description		
x = 4.0-0	Single chip, unmounted, chip size 4.0mm x 4.0mm		
x = 1.0b-0	Batch of 4 unmounted chips, chip size 1.0mm x 1.0mm		
x = 1.3b-0	Batch of 4 unmounted chips, chip size 1.3mm x 1.3mm		
x = 4.0-12.7g-c / 4.0-12.7g-e	chip size 4.0mm x 4.0mm, glued on a gold plated Cu-cylinder with 12.7 mm ∅		
x = 4.0-25.0g-c / 4.0-25.0g-e	chip size 4.0mm x 4.0mm, glued on a gold plated Cu-cylinder with 25.0 mm ⊘		
x = 4.0-25.4g-c / 4.0-25.4g-e	chip size 4.0mm x 4.0mm, glued on a gold plated Cu-cylinder with 25.4 mm ∅		
x = 4.0-12.7s-c / 4.0-12.7s-e	chip size 4.0mm x 4.0mm, soldered on a gold plated Cu-cylinder with 12.7 mm ⊘		
x = 4.0-25.0s-c / 4.0-25.0s-e	chip size 4.0mm x 4.0mm, soldered on a gold plated Cu-cylinder with 25.0 mm ∅		
x = 4.0-25.4s-c / 4.0-25.4s-e	chip size 4.0mm x 4.0mm, soldered on a gold plated Cu-cylinder with 25.4 mm \oslash		
x = 4.0-25.0w-c / 4.0-25.0w-e	chip size 4.0mm x 4.0mm, soldered on a water cooled copper heat sink with 25.0 mm diameter		
x = 4.0-25.4h-c / 4.0-25.4h-e	chip size 4.0mm x 4.0mm, thin film soldered on a water cooled copper heat sink with 25.0 mm diameter for high power application		
-c Center mounting	-e Edge mounting		
x = FC/PC / FC/APC	mounted on a 1 m monomode fiber cable with FC/PC / FC/APC connector		
	available fiber types:		
	HI 980, HI 1060, Fujikura SM98-PS-U25A (polarisation maintaining (PM) fiber)		
Other chip dimensions are also possible, please ask.			